

system, the riser card also having a memory to store one or more indications of the functionality;

the sequence of instructions to cause a driver to be loaded based, at least in part, on the one or more indications.

2. (Unchanged) The system of claim 1 wherein the riser card is coupled with the motherboard via a slot interface having pins corresponding to one or more predetermined industry standards.

3. (Amended) The system of claim 1 wherein the memory of the riser card is a read-only memory (ROM) and a boot sequence is a basic input/output system (BIOS).

4. (Unchanged) The system of claim 1 wherein the functionality comprises one or more audio codecs.

5. (Unchanged) The system of claim 1 wherein the functionality comprises one or more modem codecs.

6. (Unchanged) The system of claim 1 wherein the functionality comprises support for one or more Universal Serial Bus devices.

7. (Unchanged) The system of claim 1 wherein the functionality comprises support for one or more SMBus devices.

*Cal*  
*Exhibit*  
8. (Unchanged) The system of claim 1 wherein the functionality comprises networking functionality.

9. (Unchanged) The system of claim 1 wherein the driver is loaded by an operating system.

*CV*  
10. (Amended) A riser card comprising:  
an interface to allow the riser card to be coupled with a motherboard having a chipset coupled thereto, the motherboard further having a memory coupled thereto to store a sequence of instructions to cause a driver to be loaded based, at least in part, on one or more indications of a functionality provided to a system;

a circuit that interacts with a corresponding portion of the chipset to provide the functionality to the system; and

a memory to store the one or more indications of the functionality.

11. (Amended) The riser card of claim 10 wherein the riser card is coupled with the motherboard via a slot interface having pins corresponding to one or more predetermined industry standards.

12. (Amended) The riser card of claim 10 wherein the memory is a read-only memory (ROM) and a boot sequence is a basic input/output system (BIOS).

*Q1*  
*Cont'd.*

13. (Amended) The riser card of claim 10 wherein the functionality comprises one or more audio codecs.

14. (Amended) The riser card of claim 10 wherein the functionality comprises one or more modem codecs.

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15. (Amended) The riser card of claim 10 wherein the functionality comprises support for one or more Universal Serial Bus devices.

16. (Amended) The riser card of claim 10 wherein the functionality comprises support for one or more SMBus devices.

17. (Amended) The riser card of claim 10 wherein the functionality comprises networking functionality.

18. (Amended) The riser card of claim 10 wherein the driver is loaded by an operating system.

19. (Amended) A memory comprising an interface to couple with a riser card, the riser card having a circuit thereon that interacts with a corresponding portion of a chipset to provide a functionality to a system, the memory to store one or more indications of the functionality, wherein a driver is loaded based, at least in part, on the one or more indications of the functionality.

*Alt  
int.*

20. (Amended) The memory of claim 19 wherein the riser card is coupled with a motherboard via a slot interface having pins corresponding to one or more predetermined industry standards.

21. (Amended) The memory of claim 19 wherein the memory is a read-only memory (ROM).

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22. (Amended) The memory of claim 19 wherein the functionality comprises one or more audio codecs.

23. (Amended) The memory of claim 19 wherein the functionality comprises one or more modem codecs.

24. (Amended) The memory of claim 19 wherein the functionality comprises support for one or more Universal Serial Bus devices.

25. (Amended) The memory of claim 19 wherein the functionality comprises support for one or more SMBus devices.

26. (Amended) The memory of claim 19 wherein the functionality comprises networking functionality.